

FIG. 1

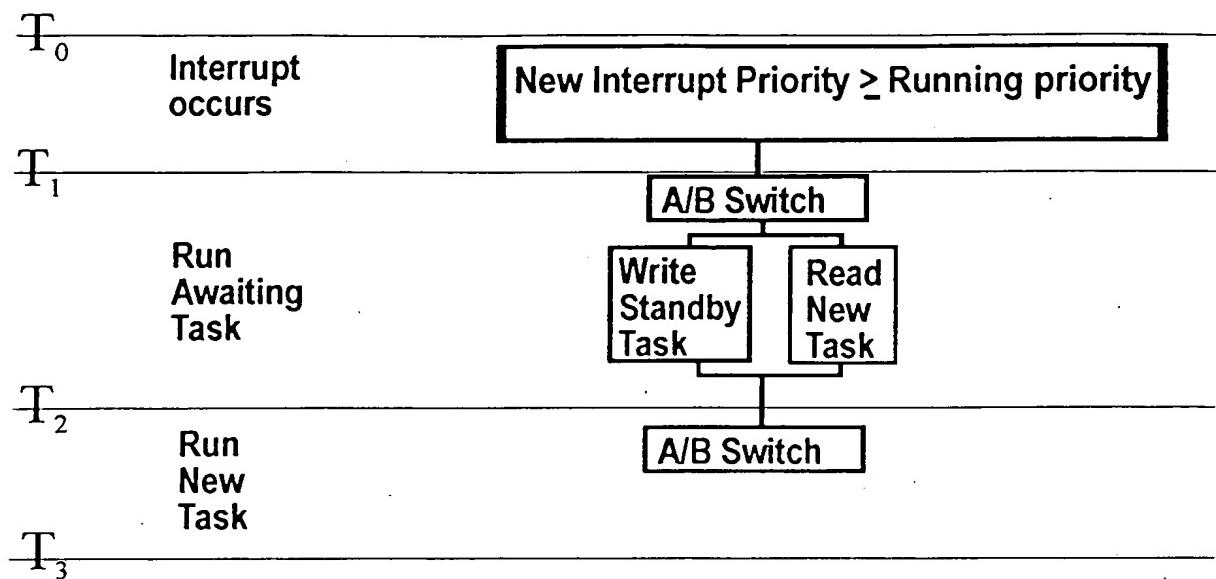


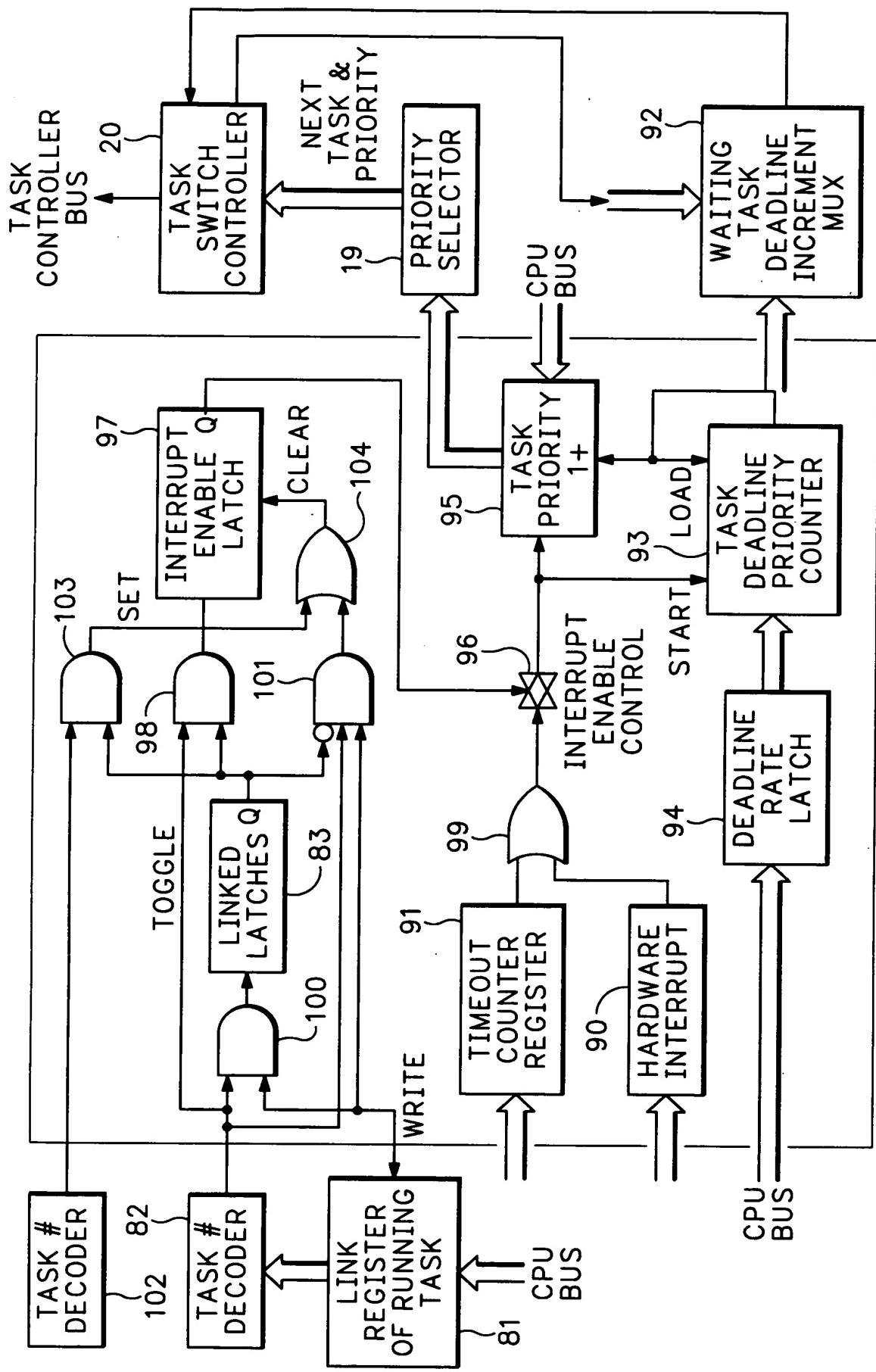
Fig. 2

Set Interrupt Enable latch if: Link Register task decoder asserted & LINKED

Clear Interrupt Enable latch IEL if: Link register task decoder & not LINKED, and write
or: Running task decoder asserted & LINKED

LINKED	write	Link register	Run task	Set/Clear IEL
1	0	1	0	SET
0	1	1	0	CLEAR
1	0	0	1	CLEAR

Fig. 3A



PER TASK
Fig.3

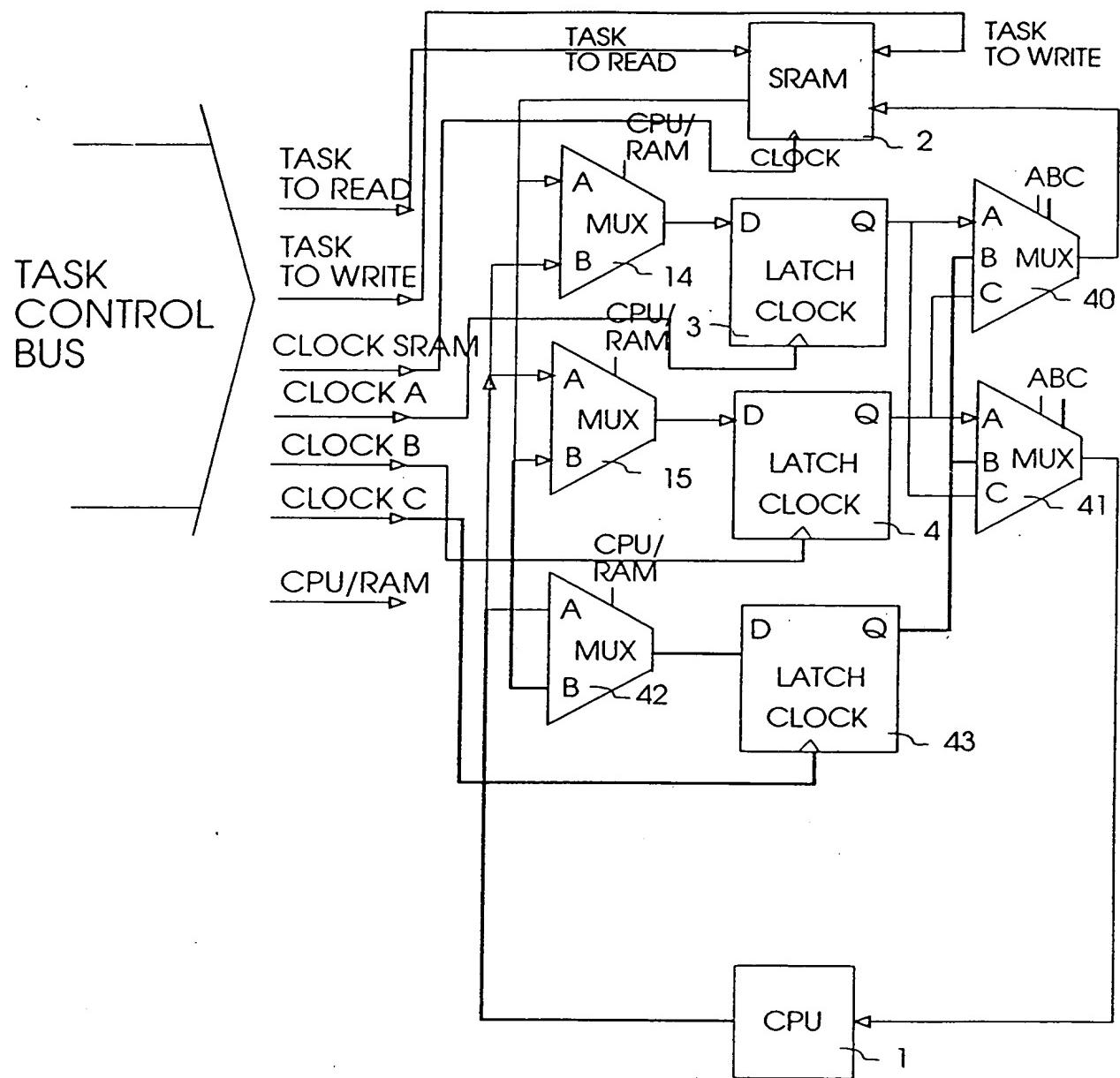


FIG. 4

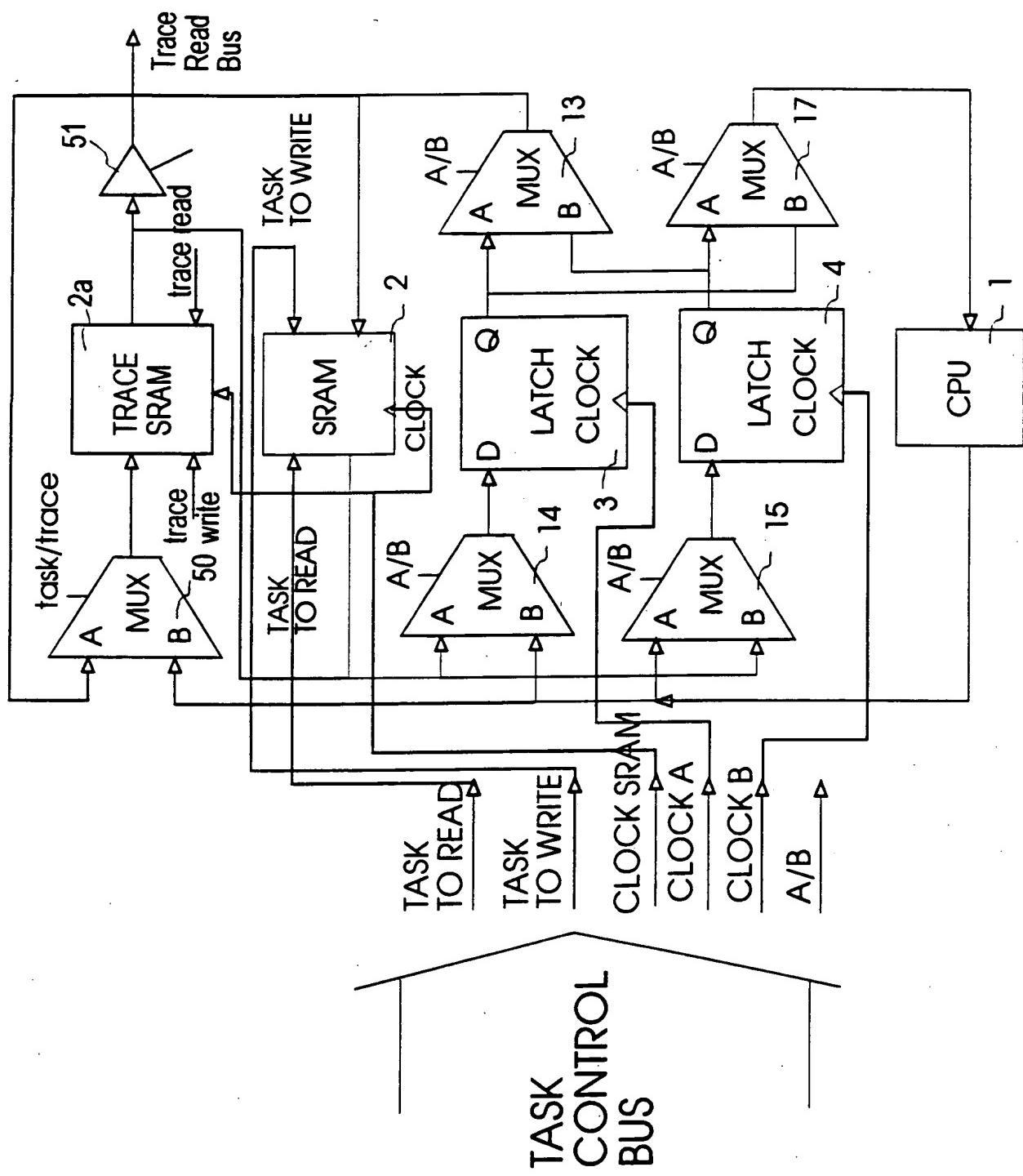


FIG. 5

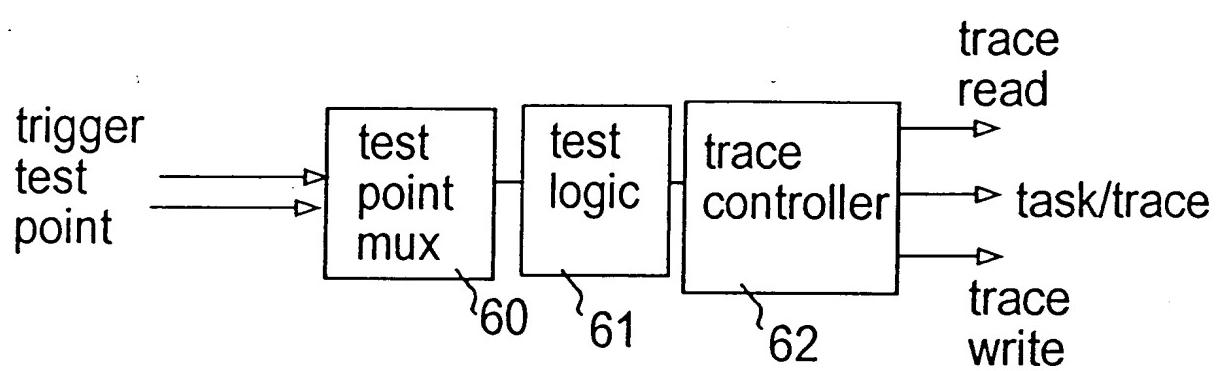


FIG. 6

Fig.7

